

**NONPROVISIONAL PATENT  
APPLICATION TRANSMITTAL RULE §1.53(b)  
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



18351 U.S. PTO  
032604

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Docket No. 108075-00126  
Date: March 26, 2004

22387 U.S.P.T.O.  
10/809894  
032604

Director of the U.S. PTO  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing under 37 C.F.R. §1.53(b) is a nonprovisional patent application:

For (Title): SEMICONDUCTOR INTEGRATED CIRCUIT AND LEVEL CONVERSION  
CIRCUIT

By (Inventors): Tomohiko KOTO (Aichi, JP)

- 37 pages of Specification/Claims 1-20/Abstract are attached.
- Formal drawings (Figs. 1-14; 7 sheets) are attached.
- A Declaration and Power of Attorney is attached.
- An assignment of the invention to FUJITSU LIMITED is attached, along with Form PTO-1595 and a check for \$40.00.
- An Information Disclosure Statement is attached, along with Form PTO-1449, and 1 reference.
- Priority of foreign application No. 2003-363136 filed October 23, 2003 in Japan is claimed under 35 U.S.C. §119.
- A certified copy of the above corresponding foreign application is attached.

The filing fee is calculated below and includes claim status after entry of any Preliminary Amendment noted above:

SMALL ENTITY			LARGE ENTITY		
FOR:	NO. FILED	NO. EXTRA	RATE	FEES	RATE
BASIC FEE				\$ 385	
TOTAL CLAIMS	20 - 20	= 0	x 9 =	\$	
INDEP CLAIMS	3 - 3	= 0	x 43 =	\$	
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+145 =	\$	
			TOTAL	\$	
			OR		
			RATE		
			\$ 770		
			OR		
			x 18		
			OR		
			x 86		
			OR		
			+290		
			OR		
			TOTAL		
			\$ 770		

- A check in the amount of \$810 (\$770 for the filing fee and \$40 for the Assignment Recordation Fee) is attached. Please charge any fee deficiency or credit any overpayment to Deposit Account No. 01-2300.

Respectfully submitted,

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CMM/cam